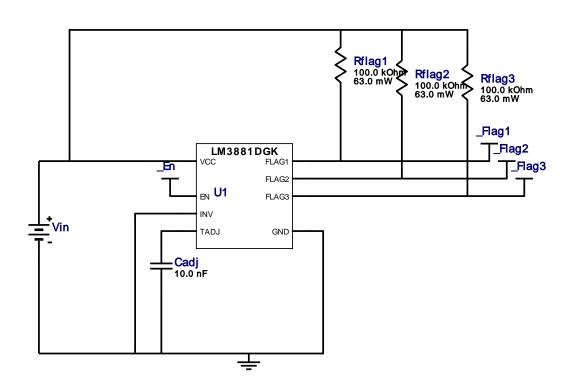


WEBENCH® Design Report

VinMin = 14.0V VinMax = 22.0V Vout = 3.3V Iout = 2.0A Device = LM3881MM/NOPB Topology = SEQUENCER Created = 7/23/16 7:05:54 AM BOM Cost = \$0.54 BOM Count = 5 Total Pd = 0.0W

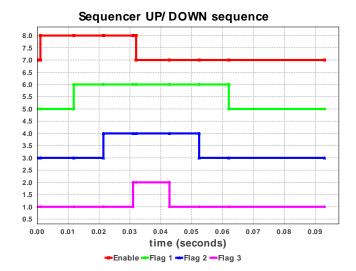
Design: 4739804/13 LM3881MM/NOPB

Design 13 - LM3881MM/NOPB



Electrical BOM

#	Name	Manufacturer	Part Number	Properties	Qty	Price	Footprint
1.	Cadj	MuRata	GRM216R71H103KA01D Series= X7R	Cap= 10.0 nF VDC= 50.0 V IRMS= 0.0 A	1	\$0.01	0805 7 mm ²
2.	Rflag1	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
3.	Rflag2	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
4.	Rflag3	Vishay-Dale	CRCW0402100KFKED Series= CRCWe3	Res= 100.0 kOhm Power= 63.0 mW Tolerance= 1.0%	1	\$0.01	0402 3 mm ²
5.	U1	Texas Instruments	LM3881MM/NOPB	Switcher	1	\$0.50	S-PDSO-G8 36 mm ²



Operating Values

operating rando								
#	Name	Value	Category	Description				
1.	BOM Count	5	General	Total Design BOM count				
2.	FootPrint	52.0 mm ²	General	Total Foot Print Area of BOM components				
3.	Total BOM	\$0.54	General	Total BOM Cost				
4.	Total Pd	240.0 μW	Power	Total Power Dissipation				
5.	Flag Voltage	3.0 V		Flag Voltage				
6.	Flag1 Down delay	30.0 ms		Flag Delay				
	(From EN high to low)							
7.	Flag1 Up delay (From	10.8 ms		Flag Delay				
	EN low to high)							
8.	Flag2 Down delay	20.4 ms		Flag Delay				
	(From EN high to low)							
9.	Flag2 Up delay (From	20.4 ms		Flag Delay				
	EN low to high)							
10.	Flag3 Down delay	10.8 ms		Flag Delay				
	(From EN high to low)							
11.	Flag3 Up delay (From	30.0 ms		Flag Delay				
	EN low to high)							
12.	Flags Used	2.0		Flags Used				
13.	Total Flags	3.0		Total Flags				
14.	Vcc	3.0 V		Vcc				

Description

Base Product Number

Design Assistance

Design Inputs

base_pn

Name

1. LM3881 Product Folder: http://www.ti.com/product/LM3881: contains the data sheet and other resources.

Value

I M3881

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